

## LVDS / LVPECL Oscillators (no PLL)

### Features

- SMD package 7,0 x 5,0 x 1,8 mm and SMD 5,0 x 3,2 x 1,2 mm
- **NO PLL**
- Excellent Phase Noise and Phase Jitter
- LVPECL/ LVDS Output
- Very Low Current Consumption

### Applications

High Speed Data Transfer, Optical Network, Ethernet

### Specifications

Item	Symbol	Specifications	Conditions
Output frequency	$f_0$	10 MHz to 200 MHz	
Supply voltage	Vcc	+2.5V and +3.3V	
Current consumption	Icc	25mA max. (125,0 MHz)	Vcc= +3.3 V
Output Wave		LVPECL/ LVDS	
Phase Jitter		166 fsec typ. (RMS, 12KHz to 20 MHz)	(125 MHz)
Stability		+50 ppm max. (default) +25 ppm max. (available)	(from -40 to +85°C)
Voltage Control (for VCXO only)		+100 ppm min.	(+1.65V +- 1.35V)

### Phase Noise (125.000 MHz)

Frequency Offset (Hz)	Phase Noise (dBc/Hz)
1 k	-116 typ.
10 k	-138 typ.
100 k	-144 typ.

\*Please consult us for customized specifications.

